80151 SIGNAL PROCESSORS

Exam Apr. 23, 2000

1. Explain shortly the following terms and abbreviations:
   a) VLIW
   b) ISS
   c) foundry-captive core
   d) COFF
   e) truncation of magnitude
   f) Harvard architecture

2. Compare opcode-operand and algebraic (C-like) assembly syntax.

3. Operands 0.75 and -0.6875 are store into memory in Q7-representation. The multiplier obtains 8-bit operands and produces 16-bit results. Such a result can be truncated in ALU to be stored into 8-bit memories. Perform the multiplication and show the quantized result stored into the memory.

4. What kind of features can be found in fixed-point signal processors for overflow management??